

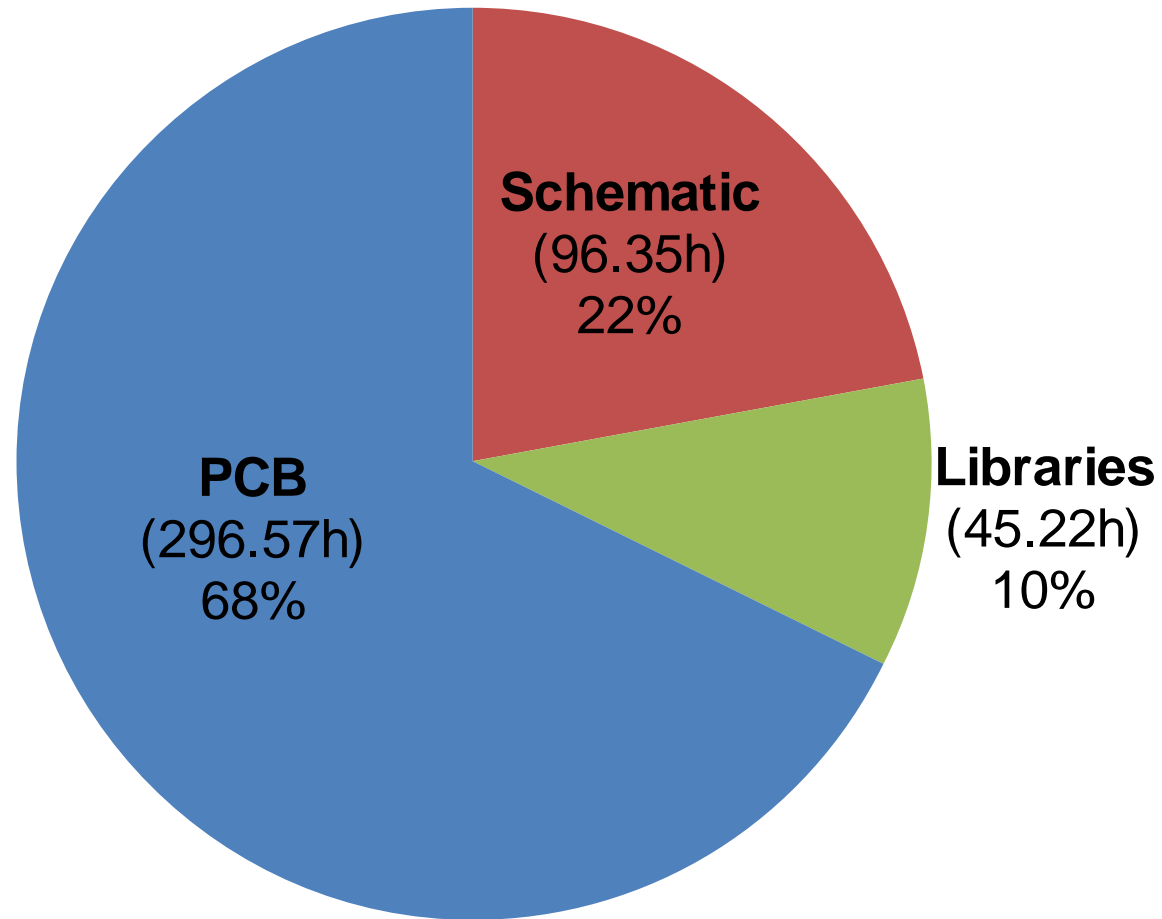
iMX6 Rex Module Time Development statistics

<http://www.imx6rex.com/>

Duration		Description
Hours	Minutes	
3	49	Specification, comparison with similar project, finding reference design
1	3	Working on schematic
5	9	Replacing pin designators for iMX processor
21	54	Changing and creating schematic
11	28	Power management, power up sequencing
7	13	Updating ports and nets
1	15	Drawing block diagram
3	48	Working on connector
2	26	Changing Ethernet PHY interface
0	57	Schematic preliminary checking
5	15	Updating and creating components
4	33	Updating and creating footprints
1	48	Fixing compiled errors and warning
0	23	Schematic checking
10	49	Preliminary Component placement
2	13	Finding handsome 3D models for components
0	35	RF: Checking schematic
5	1	Updating schematic after preliminary checking
15	15	Schematic symbol checking
31	56	Schematic browsing
3	45	Ordering components
6	50	Footprint checking
2	27	Component placement V2
7	39	Preliminary layout
17	0	Processor power fanout and preliminary layout
19	23	Memory data signal fanout, first layout
9	32	Memory address signal fanout, first layout
8	59	Ethernet PHY replacement and connection
18	21	Processor signal fanout - processor, connector J1
23	16	Processor signal fanout - processor, connector J2
15	11	Providing changes from RF checking
2	42	Finishing preliminary layout
38	0	Memory length matching - data
2	56	Working on mechanical layers
28	21	Optimizing layout
2	45	Checking conflict in IOMux
27	23	Memory length matching - address
4	2	Memory length matching - clock
26	56	Optimizing layout - inner layers
15	51	Memory layout checking
8	37	Optimizing layout - top & bottom
3	1	Manufacturing layers optimizing
6	8	Releasing board
2	13	Ordered components checking
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438h	8min	Total

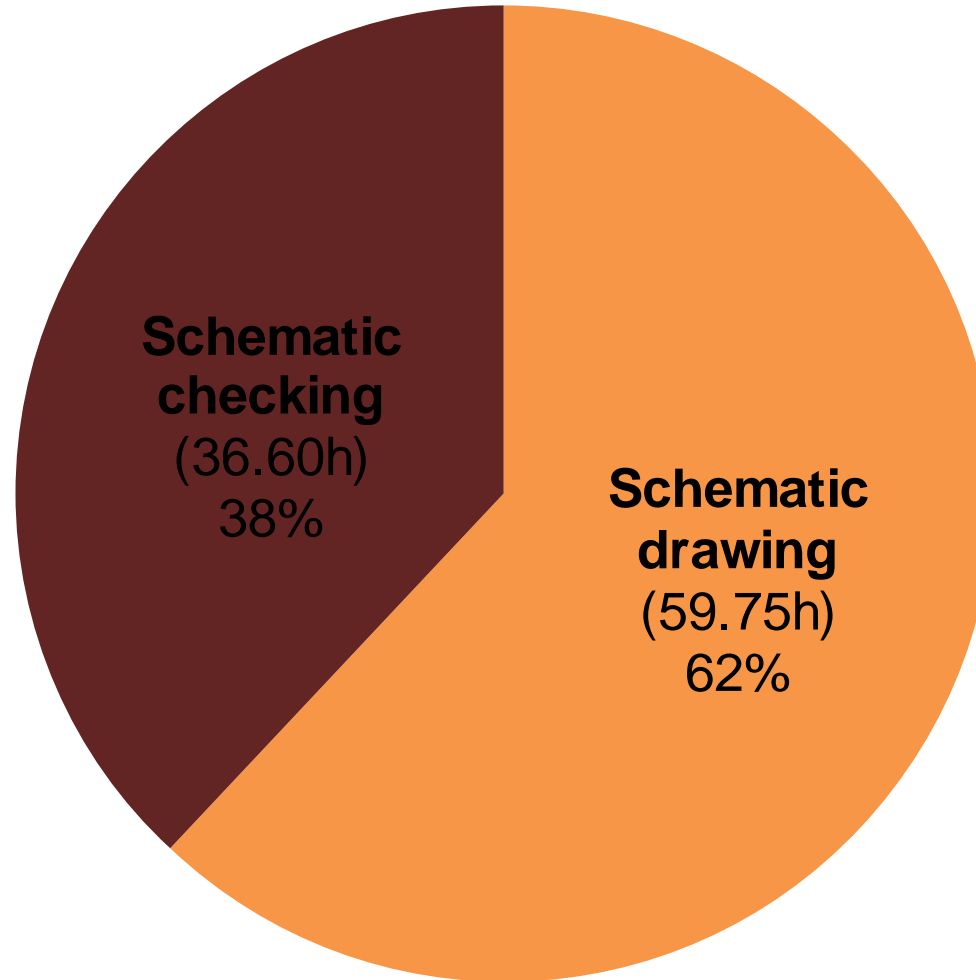
The total time needed to design the iMX6 Rex ARM Module Prototype

Total: 438.13 hours



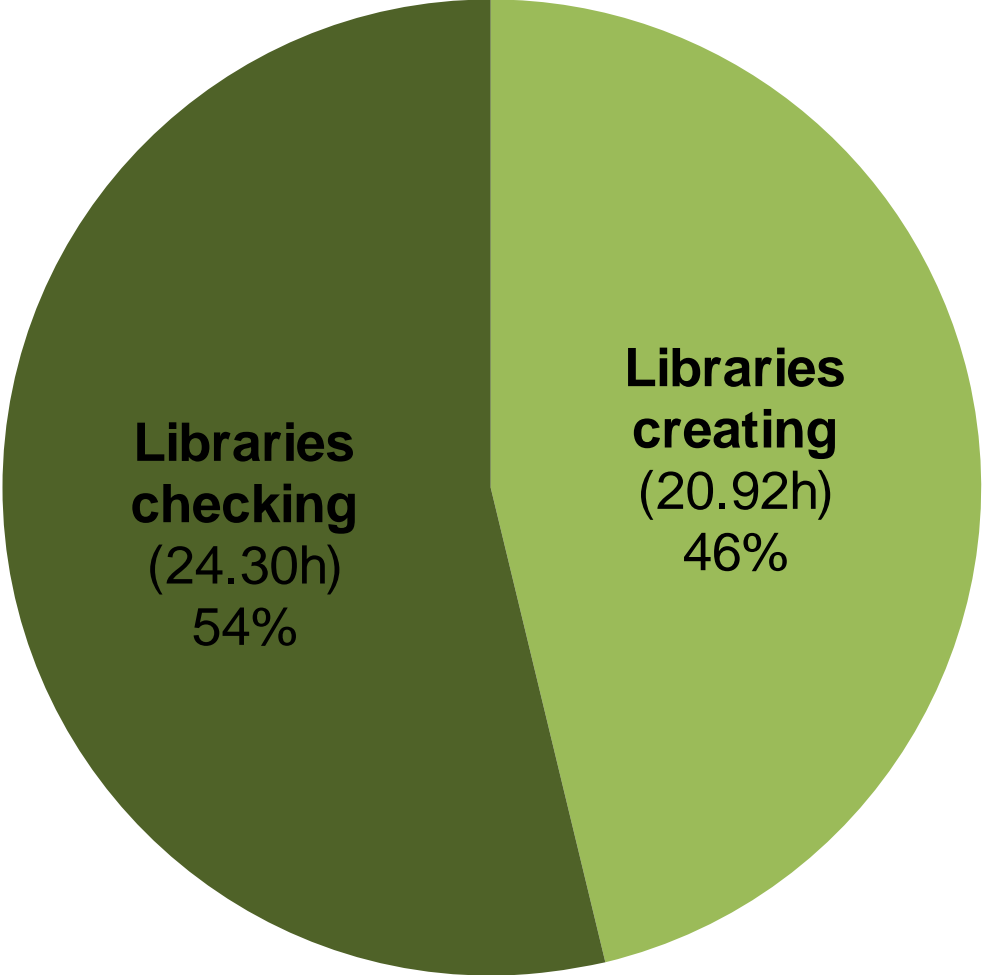
Time needed for Schematic design

Total: 96.35 hours



Time needed for Library design

Total: 45.22 hours



Time needed for PCB design

Total: 296.57 hours

